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# Department of Electrical Engineering

Second Class

Electronic I

Chapter 4 DC Biasing—BJTs Prepared by Lec 3

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# 4. 5 Voltage-Divider Bias Configuration

In the previous bias configurations, the bias current ICQ and voltage VCEQ were a function of the current gain b of the transistor. However, because b is temperature sensitive, especially for silicon transistors, and the actual value of beta is usually not well defined, it would be desirable to develop a bias circuit that is less dependent on, or in fact is independent of, the transistor beta. The voltage-divider bias configuration of Fig. 28 is such a network. If analysed on an exact basis, the sensitivity to changes in beta is quite small. If the circuit parameters are properly chosen, the resulting levels of ICQ and VCEQ can be almost totally independent of beta. Recall from previous discussions that a Q-point is defined by a fixed level of ICQ and VCEQ as shown in Fig. 29. The level of IBQ will change with the change in beta, but the operating point on the characteristics defined by ICQ and VCEQ can remain fixed if the proper circuit parameters are employed.



# exact analysis

For the dc analysis the network of Fig. 28 can be redrawn as shown in Fig. 30. The input side of the network can then be redrawn as shown in Fig. 31 for the dc analysis.

The Thevenin equivalent network for the network to the left of the base terminal can then be found in the following manner: Rth The voltage source is replaced by a short-circuit equivalent as shown in Fig. 32:



*E*th The voltage source *VCC* is returned to the network and the open-circuit Thevenin voltage of Fig. 33 determined as follows: Applying the voltage-divider rule gives

$$E_{\rm Th} = V_{R_2} = \frac{R_2 V_{CC}}{R_1 + R_2}$$
(29)

The Thevenin network is then redrawn as shown in Fig. 34, and *IBQ* can be determined by first applying Kirchhoff's voltage law in the clockwise direction for the loop indicated:

ETh - IBRTh - VBE - IERE = 0

Substituting  $IE = (\beta + 1)IB$  and solving for IB yields

$$I_B = \frac{E_{\rm Th} - V_{BE}}{R_{\rm Th} + (\beta + 1)R_E}$$
(30)

Although Eq. (30) initially appears to be different from those developed earlier, note that the numerator is again a difference of two voltage levels and the denominator is the base resistance plus the emitter resistor reflected by ( $\beta$ +1) certainly very similar to Eq. (17). Once

*IB* is known, the remaining quantities of the network can be found in the same manner as developed for the emitter-bias configuration. That is,

$$V_{CE} = V_{CC} - I_C (R_C + R_E)$$

(31)

which is exactly the same as Eq. (19). The remaining equations for *VE*, *VC*, and *VB* are also the same as obtained for the emitter-bias configuration.



## approximate analysis

The input section of the voltage-divider configuration can be represented by the network of Fig. 36. The resistance Ri is the equivalent resistance between base and ground for the transistor with an emitter resistor RE. Recall from Section 4 [Eq. (18)] that the reflected resistance between base and emitter is defined by  $Ri = (\beta + 1) RE$ . If Ri is much larger than the resistance R2, the current IB will be much smaller than I2 (current always seeks the path of least resistance) and I2 will be approximately equal to I1. If we accept the approximation that IB is essentially 0 A compared to I1 or I2, then I1 = I2, and R1 and R2 can be considered series elements. The voltage across R2, which is actually the base voltage, can be



determined using the voltage-divider rule (hence the name for the configuration). That is,

$$V_B = \frac{R_2 V_{CC}}{R_1 + R_2}$$
(32)

Because Ri = ( $\beta$  + 1) RE  $\approx \beta$ RE the condition that will define whether the approximate approach can be applied is

$$\beta R_E \ge 10 R_2 \tag{33}$$

In other words, if  $\beta$  times the value of *RE* is at least 10 times the value of *R*2, the approximate approach can be applied with a high degree of accuracy. Once *VB* is determined, the level of *VE* can be calculated from

$$V_E = V_B - V_{BE} \tag{34}$$

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and the emitter current can be determined from

$$I_E = \frac{V_E}{R_E} \tag{35}$$

And

(36)

The collector-to-emitter voltage is determined by

VCE = VCC - ICRC - IERE

 $I_{C_0} \cong I_E$ 

but because  $IE \approx IC$ ,

 $V_{CE_{Q}} = V_{CC} - I_{C}(R_{C} + R_{E})$ (37)

Note in the sequence of calculations from Eq. (33) through Eq. (37) that b does not appear and *IB* was not calculated. The *Q*-point (as determined by *ICQ* and *VCEQ*) is therefore independent of the value of  $\beta$ .

**EXAMPLE 9** Repeat the analysis of Fig. 35 using the approximate technique, and compare solutions for  $I_{C_0}$  and  $V_{CE_0}$ .

Solution: Testing:

 $\beta R_E \ge 10R_2$   $(100)(1.5 \text{ k}\Omega) \ge 10(3.9 \text{ k}\Omega)$   $150 \text{ k}\Omega \ge 39 \text{ k}\Omega \text{ (satisfied)}$ 

Eq. (32): 
$$V_B = \frac{R_2 V_{CC}}{R_1 + R_2}$$
  
=  $\frac{(3.9 \text{ k}\Omega)(22 \text{ V})}{39 \text{ k}\Omega + 3.9 \text{ k}\Omega}$   
= 2 V

Eq. (34): 
$$V_E = V_B - V_{BE}$$
  
= 2 V - 0.7 V  
= 1.3 V  
 $I_{CQ} \approx I_E = \frac{V_E}{R_E} = \frac{1.3 \text{ V}}{1.5 \text{ k}\Omega} = 0.867 \text{ mA}$ 

compared to 0.84 mA with the exact analysis. Finally,

$$V_{CE_Q} = V_{CC} - I_C(R_C + R_E)$$
  
= 22 V - (0.867 mA)(10 kV + 1.5 kΩ)  
= 22 V - 9.97 V  
= 12.03 V

**EXAMPLE 10** Repeat the exact analysis of Example 8 if  $\beta$  is reduced to 50, and compare solutions for  $I_{C_0}$  and  $V_{CE_0}$ .

**Solution:** This example is not a comparison of exact versus approximate methods, but a testing of how much the *Q*-point will move if the level of  $\beta$  is cut in half.  $R_{\text{Th}}$  and  $E_{\text{Th}}$  are the same:

$$R_{\text{Th}} = 3.55 \text{ k}\Omega, \qquad E_{\text{Th}} = 2 \text{ V}$$

$$I_B = \frac{E_{\text{Th}} - V_{BE}}{R_{\text{Th}} + (\beta + 1)R_E}$$

$$= \frac{2 \text{ V} - 0.7 \text{ V}}{3.55 \text{ k}\Omega + (51)(1.5 \text{ k}\Omega)} = \frac{1.3 \text{ V}}{3.55 \text{ k}\Omega + 76.5 \text{ k}\Omega}$$

$$= 16.24 \,\mu\text{A}$$

$$I_{C_Q} = \beta I_B$$

$$= (50)(16.24 \,\mu\text{A})$$

$$= 0.81 \text{ mA}$$

$$V_{CE_Q} = V_{CC} - I_C(R_C + R_E)$$

$$= 22 \text{ V} - (0.81 \text{ mA})(10 \text{ k}\Omega + 1.5 \text{ k}\Omega)$$

$$= 12.69 \text{ V}$$

Tabulating the results, we have:

Effect of  $\beta$  variation on the response of the voltage-divider configuration of Fig. 35.

β	$I_{C_Q}(mA)$	$V_{CE_Q}(V)$	
100	0.84 mA	12.34 V	
50	0.81 mA	12.69 V	

The results clearly show the relative insensitivity of the circuit to the change in b. Even though  $\beta$  is drastically cut in half, from 100 to 50, the levels of ICQ and VCEQ are essentially the same

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**EXAMPLE 11** Determine the levels of  $I_{C_Q}$  and  $V_{CE_Q}$  for the voltage-divider configuration of Fig. 37 using the exact and approximate techniques and compare solutions. In this case, the conditions of Eq. (33) *will not be satisfied* and the results will reveal the difference in solution if the criterion of Eq. (33) is ignored.



FIG. 37 Voltage-divider configuration for Example 11.

**Solution:** Exact analysis:  
Eq. (33):  

$$\beta R_E \ge 10R_2$$
  
(50)(1.2 k $\Omega$ )  $\ge 10(22 k\Omega)$   
 $60 k\Omega \ne 220 k\Omega$  (not satisfied)  
 $R_{Th} = R_1 ||R_2 = 82 k\Omega ||22 k\Omega = 17.35 k\Omega$   
 $E_{Th} = \frac{R_2 V_{CC}}{R_1 + R_2} = \frac{22 k\Omega(18 V)}{82 k\Omega + 22 k\Omega} = 3.81 V$   
 $I_B = \frac{E_{Th} - V_{BE}}{R_{Th} + (\beta + 1)R_E} = \frac{3.81 V - 0.7 V}{17.35 k\Omega + (51)(1.2 k\Omega)} = \frac{3.11 V}{78.55 k\Omega} = 39.6 \,\mu\text{A}$   
 $I_{C\varrho} = \beta I_B = (50)(39.6 \,\mu\text{A}) = 1.98 \,\text{mA}$   
 $V_{CE\varrho} = V_{CC} - I_C(R_C + R_E)$   
 $= 18 V - (1.98 \,\text{mA})(5.6 \,\text{k}\Omega + 1.2 \,\text{k}\Omega)$   
 $= 4.54 V$ 

Approximate analysis:

$$V_B = E_{\text{Th}} = 3.81 \text{ V}$$

$$V_E = V_B - V_{BE} = 3.81 \text{ V} - 0.7 \text{ V} = 3.11 \text{ V}$$

$$I_{C_Q} \cong I_E = \frac{V_E}{R_E} = \frac{3.11 \text{ V}}{1.2 \text{ k}\Omega} = 2.59 \text{ mA}$$

$$V_{CE_Q} = V_{CC} - I_C(R_C + R_E)$$

$$= 18 \text{ V} - (2.59 \text{ mA})(5.6 \text{ k}\Omega + 1.2 \text{ k}\Omega)$$

$$= 3.88 \text{ V}$$

## **Transistor Saturation**

The output collector–emitter circuit for the voltage-divider configuration has the same appearance as the emitter-biased circuit analysed in Section 4. The resulting equation for the saturation current (when *VCE* is set to 0 V on the schematic) is therefore the same as obtained for the emitter-biased configuration. That is,

(38)

$$I_{C_{\rm sat}} = I_{C_{\rm max}} = \frac{V_{CC}}{R_C + R_E}$$

#### Load-Line analysis

The similarities with the output circuit of the emitter-biased configuration result in the same intersections for the load line of the voltage-divider configuration. The load line will therefore, have the same appearance as that of Fig. 25, with

$$I_{C} = \frac{V_{CC}}{R_{C} + R_{E}} \Big|_{V_{CE} = 0 \text{ V}}$$
(39)  
$$V_{CE} = V_{CC} \Big|_{I_{C} = 0 \text{ mA}}$$
(40)

The level of *IB* is of course determined by a different equation for the voltage-divider bias and the emitter-bias configurations.

#### 6.4 Collector Feedback Configuration

An improved level of stability can also be obtained by introducing a feedback path from collector to base as shown in Fig. 38. Although the Q-point is not totally independent of beta (even under approximate conditions), the sensitivity to changes in beta or temperature variations are normally less than encountered for the fixed-bias or emitter-biased configurations. The analysis will again be performed by first analysing the base–emitter loop, with the results then applied to the collector–emitter loop.

base–emitter Loop Figure 39 shows the base–emitter loop for the voltage feedback configuration. Writing Kirchhoff's voltage law around the indicated loop in the clockwise direction will result in

VCC - ICRC - IBRF - VBE - IERE = 0

It is important to note that the current through RC is not IC, but IC (where  $IC^- = IC + IB$ ). However, the level of IC and  $IC^-$  far exceeds the usual level of IB, and the approximation  $IC^-\approx IC$  is normally employed. Substituting  $IC^-\approx IC = \beta IB$  and  $IE \approx IC$  results in VCC -  $\beta IBRC$  - IBRF - VBE -  $\beta IBRE = 0$ 



DC bias circuit with voltage feedback.

Base-emitter loop for the network of Fig. 38.

Gathering terms, we have  $V_{CC} - V_{BE} - \beta I_B (R_C + R_E) - I_B R_F = 0$ and solving for  $I_B$  yields  $I_B = \frac{V_{CC} - V_{BE}}{R_F + \beta (R_C + R_E)}$ (41)

## **Collector-emitter Loop**

The collector–emitter loop for the network of Fig. 38 is provided in Fig. 40. Applying Kirchhoff's voltage law around the indicated loop in the clockwise direction results in

 $IERE + VCE + IC^{-}RC - VCC = 0$ Because  $I'_{C} \cong I_{C}$  and  $I_{E} \cong I_{C}$ , we have  $I_{C}(R_{C} + R_{E}) + V_{CE} - V_{CC} = 0$ and  $V_{CE} = V_{CC} - I_{C}(R_{C} + R_{E})$ (42)

which is exactly as obtained for the emitter-bias and voltage-divider bias configurations.

 $I_{C}$  +  $R_{C}$  -  $I_{C}$  +  $V_{CE}$  +  $R_{E}$  - +  $R_{E}$  -

FIG. 40 Collector–emitter loop for the network of Fig. 38.

**EXAMPLE 12** Determine the quiescent levels of  $I_{C_Q}$  and  $V_{CE_Q}$  for the network of Fig. 41.



Network for Example 12.

Solution: Eq. (41): 
$$I_B = \frac{V_{CC} - V_{BE}}{R_F + \beta(R_C + R_E)}$$
  
 $= \frac{10 \text{ V} - 0.7 \text{ V}}{250 \text{ k}\Omega + (90)(4.7 \text{ k}\Omega + 1.2 \text{ k}\Omega)}$   
 $= \frac{9.3 \text{ V}}{250 \text{ k}\Omega + 531 \text{ k}\Omega} = \frac{9.3 \text{ V}}{781 \text{ k}\Omega}$   
 $= 11.91 \mu\text{A}$   
 $I_{C_Q} = \beta I_B = (90)(11.91 \mu\text{A})$   
 $= 1.07 \text{ mA}$   
 $V_{CE_Q} = V_{CC} - I_C(R_C + R_E)$   
 $= 10 \text{ V} - (1.07 \text{ mA})(4.7 \text{ k}\Omega + 1.2 \text{ k}\Omega)$   
 $= 10 \text{ V} - 6.31 \text{ V}$   
 $= 3.69 \text{ V}$ 

**EXAMPLE 14** Determine the dc level of  $I_B$  and  $V_C$  for the network of Fig. 42.



Network for Example 14.

**Solution:** In this case, the base resistance for the dc analysis is composed of two resistors with a capacitor connected from their junction to ground. For the dc mode, the capacitor assumes the open-circuit equivalence, and  $R_B = R_{F_1} + R_{F_2}$ .

Solving for  $I_B$  gives

$$I_B = \frac{V_{CC} - V_{BE}}{R_B + \beta(R_C + R_E)}$$
  
=  $\frac{18 \text{ V} - 0.7 \text{ V}}{(91 \text{ k}\Omega + 110 \text{ k}\Omega) + (75)(3.3 \text{ k}\Omega + 0.51 \text{ k}\Omega)}$   
=  $\frac{17.3 \text{ V}}{201 \text{ k}\Omega + 285.75 \text{ k}\Omega} = \frac{17.3 \text{ V}}{486.75 \text{ k}\Omega}$   
=  $35.5 \mu\text{A}$   
 $I_C = \beta I_B$   
=  $(75)(35.5 \mu\text{A})$   
=  $2.66 \text{ mA}$   
 $V_C = V_{CC} - I_C'R_C \cong V_{CC} - I_CR_C$   
=  $18 \text{ V} - (2.66 \text{ mA})(3.3 \text{ k}\Omega)$   
=  $18 \text{ V} - 8.78 \text{ V}$   
=  $9.22 \text{ V}$ 

## **Saturation Conditions**

Using the approximation  $IC^- = IC$ , we find that the equation for the saturation current is the

same as obtained for the voltage-divider and emitter-bias configurations. That is,

$$I_{C_{\text{sat}}} = I_{C_{\text{max}}} = \frac{V_{CC}}{R_C + R_E} \tag{43}$$

### Load-Line analysis

Continuing with the approximation  $IC^- = IC$  results in the same load line defined for the voltage-divider and emitter-biased configurations. The level of IBQ is defined by the chosen bias configuration.

**EXAMPLE 15** Given the network of Fig. 43 and the BJT characteristics of Fig. 44.

a. Draw the load line for the network on the characteristics.

b. Determine the dc beta in the center region of the characteristics. Define the chosen point as the Q-point.

c. Using the dc beta calculated in part b, find the dc value of  $I_B$ .

d. Find I<sub>CQ</sub> and I<sub>CEQ</sub>.



#### Solution:

a. The load line is drawn on Fig. 45 as determined by the following intersections:

$$V_{CE} = 0 \text{ V}: I_C = \frac{V_{CC}}{R_C + R_E} = \frac{36 \text{ V}}{2.7 \text{ k}\Omega + 330 \Omega} = 11.88 \text{ mA}$$
  
 $I_C = 0 \text{ mA}: V_{CE} = V_{CC} = 36 \text{ V}$ 



Defining the Q-point for the voltage-divider bias configuration of Fig. 43.

b. The dc beta was determined using  $I_B = 25 \,\mu\text{A}$  and  $V_{CE}$  about 17 V.

$$\beta \simeq \frac{I_{C_Q}}{I_{B_Q}} = \frac{6.2 \text{ mA}}{25 \,\mu\text{A}} = 248$$

c. Using Eq. 41:

$$I_B = \frac{V_{CC} - V_{BE}}{R_B + \beta(R_C + R_E)} = \frac{36 \text{ V} - 0.7 \text{ V}}{510 \text{ k}\Omega + 248(2.7 \text{ k}\Omega + 330 \Omega)}$$
$$= \frac{35.3 \text{ V}}{510 \text{ k}\Omega + 751.44 \text{ k}\Omega}$$
and  $I_B = \frac{35.3 \text{ V}}{1.261 \text{ M}\Omega} = 28 \mu \text{A}$ 

- d. From Fig. 45 the quiescent values are
  - $I_{C_Q} \cong 6.9 \,\mathrm{mA}$  and  $V_{CE_Q} \cong 15 \,\mathrm{V}$

#### 7.4 Emitter-Follower Configuration

The previous sections introduced configurations in which the output voltage is typically taken off the collector terminal of the BJT. This section will examine a configuration where the output is taken off the emitter terminal as shown in Fig. 46. The configuration of Fig. 46 is not the only one where the output can be taken off the emitter terminal. In fact, any of the configurations just described can be used so long as there is a resistor in the emitter leg.



FIG. 46 Common-collecter (emitter-follower) configuration.

The dc equivalent of the network of Fig. 46 appears in Fig. 47 Applying Kirchhoff's voltage rule to the input circuit will result in

 $I_B =$ 

$$I_B R_B - V_{BE} - I_E R_E + V_{EE} = 0$$

 $I_B R_B + (\beta + 1) I_B R_E = V_{EE} - V_{BE}$ 

and using  $I_E = (\beta + 1)I_B$ 

so that

$$=\frac{V_{EE}-V_{BE}}{R_B+(\beta+1)R_E}$$

For the output network, an application of Kirchhoff's voltage law will result in

$$-V_{CE} - I_E R_E + V_{EE} = 0$$

and

$$V_{CE} = V_{EE} - I_E R_E \tag{45}$$



(44)

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dc equivalent of Fig. 46. **EXAMPLE 16** Determine  $V_{CE_Q}$  and  $I_{E_Q}$  for the network of Fig. 48.



#### Solution:

Eq. 44:

and

44:  

$$I_{B} = \frac{V_{EE} - V_{BE}}{R_{B} + (\beta + 1)R_{E}}$$

$$= \frac{20 \text{ V} - 0.7 \text{ V}}{240 \text{ k}\Omega + (90 + 1)2 \text{ k}\Omega} = \frac{19.3 \text{ V}}{240 \text{ k}\Omega + 182 \text{ k}\Omega}$$

$$= \frac{19.3 \text{ V}}{422 \text{ k}\Omega} = 45.73 \,\mu\text{A}$$
Eq. 45:  

$$V_{CE_{Q}} = V_{EE} - I_{E}R_{E}$$

$$= V_{EE} - (\beta + 1)I_{B}R_{E}$$

$$= 20 \text{ V} - (90 + 1)(45.73 \,\mu\text{A})(2 \text{ k}\Omega)$$

$$= 20 \text{ V} - 8.32 \text{ V}$$

$$= 11.68 \text{ V}$$

$$I_{E_{Q}} = (\beta + 1)I_{B} = (91)(45.73 \,\mu\text{A})$$

$$= 4.16 \text{ mA}$$

# **8.4 Common-Base Configuration**

The common-base configuration is unique in that the applied signal is connected to the emitter terminal and the base is at, or just above, ground potential. It is a fairly popular configuration because in the ac domain it has a very low input impedance, high output impedance, and good gain.

A typical common-base configuration appears in Fig. 49. Note that two supplies are used in this configuration and the base is the common terminal between the input emitter terminal and output collector terminal. The dc equivalent of the input side of Fig. 49 appears in Fig.50.



Applying Kirchhoff's voltage law will result in

-VEE + IERE + VBE = 0  $I_E = \frac{V_{EE} - V_{BE}}{R_E}$ (46) Applying Kirchhoff's voltage law to the entire outside perimeter of the network of Fig. 51 will result in -VEE + IERE + VCE + ICRC - VCC = 0and solving for VCE: VCE = VEE + VCC - IERE - ICRC Because IE  $\approx$  IC  $V_{CE} = V_{EE} + V_{CC} - I_E(R_C + R_E)$ (47)

The voltage *VCB* of Fig. 51 can be found by applying Kirchhoff's voltage law to the output loop of Fig 51 to obtain:  $V_{CE}$ 

VCB + ICRC - VCC = 0or VCB = VCC - ICRCUsing  $IC \approx IE$ 

$$V_{CB} = V_{CC} - I_C R_C$$

![](_page_16_Figure_8.jpeg)

**EXAMPLE 17** Determine the currents  $I_E$  and  $I_B$  and the voltages  $V_{CE}$  and  $V_{CB}$  for the common-base configuration of Fig. 52.

![](_page_17_Figure_4.jpeg)

![](_page_17_Figure_5.jpeg)

Solution: Eq. 46:  

$$I_E = \frac{V_{EE} - V_{BE}}{R_E}$$

$$= \frac{4 \text{ V} - 0.7 \text{ V}}{1.2 \text{ k}\Omega} = 2.75 \text{ mA}$$

$$I_B = \frac{I_E}{\beta + 1} = \frac{2.75 \text{ mA}}{60 + 1} = \frac{2.75 \text{ mA}}{61}$$

$$= 45.08 \mu\text{A}$$
Eq. 47:  

$$V_{CE} = V_{EE} + V_{CC} - I_E(R_C + R_E)$$

$$= 4 \text{ V} + 10 \text{ V} - (2.75 \text{ mA})(2.4 \text{ k}\Omega + 1.2 \text{ k}\Omega)$$

$$= 14 \text{ V} - (2.75 \text{ mA})(3.6 \text{ k}\Omega)$$

$$= 14 \text{ V} - 9.9 \text{ V}$$

$$= 4.1 \text{ V}$$
Eq. 48:  

$$V_{CB} = V_{CC} - I_C R_C = V_{CC} - \beta I_B R_C$$

$$= 10 \text{ V} - (60)(45.08 \mu\text{A})(24 \text{ k}\Omega)$$

$$= 10 \text{ V} - 6.49 \text{ V}$$

$$= 3.51 \text{ V}$$

# **9** Miscellaneous Bias Configurations

There are a number of BJT bias configurations that do not match the basic mold of those analysed in the previous sections. In fact, there are variations in design that would require many more pages than is possible in a single publication. However, the primary purpose here is to emphasize those characteristics of the device that permit a dc analysis of the configuration and to establish a general procedure toward the desired solution. For each configuration discussed thus far, the first step has been the derivation of an expression for the base current. Once the base current is known, the collector current and voltage levels of the output circuit can be determined quite directly. This is not to imply that all solutions will take this path, but it does suggest a possible route to follow if a new configuration is encountered. The first example is simply one where the emitter resistor has been dropped from the voltage-feedback configuration of Fig. 38. The analysis is quite similar, but does require dropping *RE* from the applied equation.

**EXAMPLE 18** For the network of Fig. 53:

- a. Determine *I<sub>CQ</sub>* and *V<sub>CEQ</sub>*.
  b. Find *V<sub>B</sub>*, *V<sub>C</sub>*, *V<sub>E</sub>*, and *V<sub>BC</sub>*.

![](_page_18_Figure_8.jpeg)

Collector feedback with  $R_E = 0 \Omega$ .

#### Solution:

a. The absence of  $R_E$  reduces the reflection of resistive levels to simply that of  $R_C$ , and the equation for  $I_B$  reduces to

$$I_B = \frac{V_{CC} - V_{BE}}{R_B + \beta R_C}$$
  
=  $\frac{20 \text{ V} - 0.7 \text{ V}}{680 \text{ k}\Omega + (120)(4.7 \text{ k}\Omega)} = \frac{19.3 \text{ V}}{1.244 \text{ M}\Omega}$   
= 15.51  $\mu$ A  
 $I_{C_Q} = \beta I_B = (120)(15.51 \mu$ A)  
= 1.86 mA  
 $V_{CE_Q} = V_{CC} - I_C R_C$   
= 20 V - (1.86 mA)(4.7 k $\Omega$ )  
= 11.26 V  
 $V_B = V_{BE} = 0.7 \text{ V}$   
 $V_C = V_{CE} = 11.26 \text{ V}$   
 $V_E = 0 \text{ V}$   
 $V_{BC} = V_B - V_C = 0.7 \text{ V} - 11.26 \text{ V}$   
= -10.56 V

b.

**EXAMPLE 19** Determine  $V_C$  and  $V_B$  for the network of Fig. 54.

![](_page_19_Figure_8.jpeg)

**Solution:** Applying Kirchhoff's voltage law in the clockwise direction for the base–emitter loop results in  $-I_B R_B - V_{BE} + V_{EE} = 0$ 

 $I_B = \frac{V_{EE} - V_{BE}}{R_B}$ 

and

Substitution yields

$$I_B = \frac{9 \text{ V} - 0.7 \text{ V}}{100 \text{ k}\Omega}$$
$$= \frac{8.3 \text{ V}}{100 \text{ k}\Omega}$$
$$= 83 \mu\text{A}$$

$$I_{C} = \beta I_{B}$$
  
= (45)(83 \mu A)  
= 3.735 mA  
$$V_{C} = -I_{C}R_{C}$$
  
= -(3.735 mA)(1.2 k\Omega)  
= -4.48 V  
$$V_{B} = -I_{B}R_{B}$$
  
= -(83 \mu A)(100 k\Omega)  
= -8.3 V

**EXAMPLE 20** Determine  $V_C$  and  $V_B$  for the network of Fig. 55.

![](_page_21_Figure_4.jpeg)

![](_page_21_Figure_5.jpeg)

*Solution:* The Thévenin resistance and voltage are determined for the network to the left of the base terminal as shown in Figs. 56 and 57.

**R**<sub>Th</sub>

$$R_{\rm Th} = 8.2 \,\mathrm{k}\Omega \,\|\, 2.2 \,\mathrm{k}\Omega = 1.73 \,\mathrm{k}\Omega$$

![](_page_21_Figure_9.jpeg)

ETh

$$I = \frac{V_{CC} + V_{EE}}{R_1 + R_2} = \frac{20 \text{ V} + 20 \text{ V}}{8.2 \text{ k}\Omega + 2.2 \text{ k}\Omega} = \frac{40 \text{ V}}{10.4 \text{ k}\Omega}$$
  
= 3.85 mA  
$$E_{\text{Th}} = IR_2 - V_{EE}$$
  
= (3.85 mA)(2.2 k\Omega) - 20 V  
= -11.53 V

The network can then be redrawn as shown in Fig. 58, where the application of Kirchhoff's voltage law results in

$$-E_{\mathrm{Th}} - I_B R_{\mathrm{Th}} - V_{BE} - I_E R_E + V_{EE} = 0$$

![](_page_22_Figure_7.jpeg)

**FIG. 58** Substituting the Thévenin equivalent circuit.

Substituting  $I_E = (\beta + 1)I_B$  gives  $V_{EE} - E_{\rm Th} - V_{BE} - (\beta + 1)I_BR_E - I_BR_{\rm Th} = 0$  $I_B = \frac{V_{EE} - E_{\text{Th}} - V_{BE}}{R_{\text{Th}} + (\beta + 1)R_E}$  $= \frac{20 \text{ V} - 11.53 \text{ V} - 0.7 \text{ V}}{1.73 \text{ k}\Omega + (121)(1.8 \text{ k}\Omega)}$  $=\frac{7.77 \text{ V}}{219.53 \text{ k}\Omega}$  $= 35.39 \,\mu A$  $I_C = \beta I_B$ = (120)(35.39  $\mu$ A)  $= 4.25 \, \text{mA}$  $V_C = V_{CC} - I_C R_C$  $= 20 \text{ V} - (4.25 \text{ mA})(2.7 \text{ k}\Omega)$ = 8.53 V $V_B = -E_{\rm Th} - I_B R_{\rm Th}$  $= -(11.53 \text{ V}) - (35.39 \,\mu\text{A})(1.73 \,\text{k}\Omega)$ = -11.59 V

and

Туре	Configuration	Pertinent Equations
Fixed-bias		$I_B = \frac{V_{CC} - V_{BE}}{R_B}$ $I_C = \beta I_B, I_E = (\beta + 1)I_B$ $V_{CE} = V_{CC} - I_C R_C$
Emitter-bias		$I_B = \frac{V_{CC} - V_{BE}}{R_B + (\beta + 1)R_E}$ $I_C = \beta I_B, I_E = (\beta + 1)I_B$ $R_i = (\beta + 1)R_E$ $V_{CE} = V_{CC} - I_C (R_C + R_E)$
Voltage-divider bias	$\begin{array}{c} \bullet V_{CC} \\ R_1 \\ R_2 \\ R_2 \\ R_E \end{array}$	EXACT: $R_{\text{Th}} = R_1    R_2, E_{\text{Th}} = \frac{R_2 V_{CC}}{R_1 + R_2}$ APPROXIMATE: $\beta R_E \ge 10R_2$ $I_B = \frac{E_{\text{Th}} - V_{BE}}{R_{\text{Th}} + (\beta + 1)R_E}$ $V_B = \frac{R_2 V_{CC}}{R_1 + R_2}, V_E = V_B - V_{BE}$ $I_C = \beta I_B, I_E = (\beta + 1)I_B$ $I_E = \frac{V_E}{R_E}, I_B = \frac{I_E}{\beta + 1}$ $V_{CE} = V_{CC} - I_C (R_C + R_E)$ $V_{CE} = V_{CC} - I_C (R_C + R_E)$
Collector-feedback		$I_B = \frac{V_{CC} - V_{BE}}{R_F + \beta(R_C + R_E)}$ $I_C = \beta I_B, I_E = (\beta + 1)I_B$ $V_{CE} = V_{CC} - I_C (R_C + R_E)$
Emitter-follower		$I_B = \frac{V_{EE} - V_{BE}}{R_B + (\beta + 1)}$ $I_C = \beta I_B, I_E = (\beta + 1)I_B$ $V_{CE} = V_{EE} - I_E R_E$
Common-base		$I_E = \frac{V_{EE} - V_{BE}}{R_E}$ $I_B = \frac{I_E}{\beta + 1}, I_C = \beta I_B$ $V_{CE} = V_{EE} + V_{CC} - I_E(R_C + R_E)$ $V_{CB} = V_{CC} - I_C R_C$

 TABLE 1

 BJT Bias Configurations